### **Modeling Concurrent Systems**

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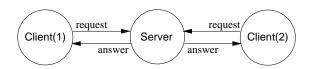
#### 1. Checking a Client/Server System with SPIN

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## A Client/Server System





- System of one server and two clients.
  - Three concurrently executing system components.
- Server manages a resource.
  - An object that only one system component may use at any time.
- Clients request resource and, having received an answer, use it.
  - Server ensures that not both clients use resource simultaneously.
  - Server eventually answers every request.

#### Set of system requirements.

### **System Implementation**



```
Server:
                                            Client(ident):
                                              param ident
  local given, waiting, sender
begin
                                            begin
  given := 0; waiting := 0
                                              loop
  1000
    sender := receiveRequest()
                                                sendRequest()
    if sender = given then
                                                receiveAnswer()
      if waiting = 0 then
                                                ... // critical region
        given := 0
                                                sendRequest()
      else
                                              endloop
        given := waiting; waiting := 0
                                            end Client
        sendAnswer(given)
      endif
    elsif given = 0 then
      given := sender
      sendAnswer(given)
    else
      waiting := sender
    endif
  endloop
end Server
```

### **Desired System Properties**



- Property: mutual exclusion.
  - At no time, both clients are in critical region.
    - Critical region: program region after receiving resource from server and before returning resource to server.
  - The system shall only reach states, in which mutual exclusion holds.
- Property: no starvation.
  - Always when a client requests the resource, it eventually receives it.
  - Always when the system reaches a state, in which a client has requested a resource, it shall later reach a state, in which the client receives the resource.
- Problem: each system component executes its own program.
  - Multiple program states exist at each moment in time.
  - Total system state is combination of individual program states.
  - Not easy to see which system states are possible.

#### How can we check that the system has the desired properties?

### Implementing the System in PROMELA



```
/* definition of a constant MESSAGE */
                                           /* the client process type */
mtype = { MESSAGE };
                                           proctype client(byte id)
/* two arrays of channels of size 2,
                                             do :: true ->
   each channel has a buffer size 1 */
                                               request[id-1] ! MESSAGE;
chan request[2] = [1] of { mtype };
chan answer [2] = [1] of { mtype };
                                               wait[id-1] = true;
                                               answer[id-1] ? MESSAGE;
/* two global arrays for monitoring
                                               wait[id-1] = false:
  the states of the clients */
bool inC[2] = false;
                                               inC[id-1] = true;
bool wait[2] = false;
                                               skip; // the critical region
                                               inC[id-1] = false;
/* the system of three processes */
init
                                               request[id-1] ! MESSAGE
                                             od;
 run client(1):
 run client(2):
 run server();
```

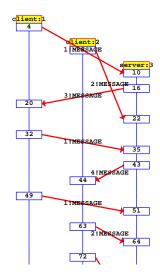
## Implementing the System in PROMELA



```
/* the server process type */
                                                /* answering the message */
proctype server()
                                                if
                                                :: sender == given ->
  /* three variables of two bit each */
                                                  if
  unsigned given : 2 = 0;
                                                  :: waiting == 0 ->
  unsigned waiting : 2 = 0;
                                                    given = 0
  unsigned sender : 2;
                                                  :: else ->
                                                    given = waiting;
  do :: true ->
                                                    waiting = 0;
                                                    answer[given-1] ! MESSAGE
    /* receiving the message */
                                                  fi;
                                                :: given == 0 ->
    if
    :: request[0] ? MESSAGE ->
                                                  given = sender;
      sender = 1
                                                  answer[given-1] ! MESSAGE
    :: request[1] ? MESSAGE ->
                                                :: else
      sender = 2
                                                  waiting = sender
    fi:
                                                fi:
                                              od;
```

# Simulating the System Execution in SPIN





## Specifying a System Property in SPIN



X-¤ Linear Time Temporal Logic Formulae		- 0 x
Formula:       (c1 && c2)		load
		LUAU
Operators:		
Property holds for: • All Executions (desired behavior) > No Executions (error behavior)		
Notes [file clientServer2-mutex.ltl]:		
		1
7		
Symbol Definitions:		
+cernie cz nic[1]		
7		
Never Claim:		Generate
<u> </u>		
* Formula As Typed: [] !(c1 && c2) * The Never Claim Below Corresponds		
* To The Negated Formula !([] 1(c1 && c2)) * (formalizing violations of the original)		
*/ (TOUMSTIZING VIOLACIONS OF ONE OFFICIAL)		
/ never : /=!(  !(c1 && c2)) */		
Verification Result: valid	Run V	erification
A varning: for plot coduction to be walld the never claim must be	e stutter-	invariant
🔲 inever claims generated from ITM. formulae are studtar-invariant		
Spin Version 4.2.2 12 December 2004)   + Partial Order Reduction		
Full statespace search for:		
never claim +		
Help Clear	Close	Save As

### Checking the System Property in SPIN



```
(Spin Version 4.2.2 -- 12 December 2004)
+ Partial Order Reduction
Full statespace search for:
never claim
assertion violations + (if within scope of claim)
acceptance cycles + (fairness disabled)
invalid end states - (disabled by never claim)
State-vector 48 byte, depth reached 477, errors: 0
     499 states, stored
     395 states, matched
     894 transitions (= stored+matched)
       0 atomic steps
hash conflicts: 0 (resolved)
Stats on memory usage (in Megabytes):
0.00user 0.01system 0:00.01elapsed 83%CPU (Oavgtext+Oavgdata Omaxresident)k
Oinputs+Ooutputs (Omajor+737minor)pagefaults Oswaps
```



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1. Checking a Client/Server System with SPIN

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### **System States**



At each moment in time, a system is in a particular state.

- $\blacksquare$  A state  $s: Var \rightarrow Val$ 
  - A state s is a mapping of every system variable x to its value s(x).
    - **Typical notation**: s = [x = 0, y = 1, ...] = [0, 1, ...]
  - Var ... the set of system variables
    - Program variables, program counters, ...
  - Val ... the set of variable values.
- The state space  $State = \{s \mid s : Var \rightarrow Val\}$ 
  - The state space is the set of possible states.
    - The system variables can be viewed as the coordinates of this space.
  - The state space may (or may not) be finite.
    - If |Var| = n and |Val| = m, then  $|State| = m^n$ .
    - $\blacksquare$  A word of  $\log_2 m^n$  bits can represent every state.

A system execution can be described by a path  $s_0 \to s_1 \to s_2 \to \dots$  in the state space.

### **Deterministic Systems**



In a sequential system, each state typically determines its successor state.

- The system is deterministic.
  - We have a (possibly not total) transition function F on states.
  - $s_1 = F(s_0)$  means " $s_1$  is the successor of  $s_0$ ".
- $\blacksquare$  Given an initial state  $s_0$ , the execution is thus determined.
  - $s_0 \to s_1 = F(s_0) \to s_2 = F(s_1) \to \dots$
- $\blacksquare$  A deterministic system (model) is a pair  $\langle I, F \rangle$ .
  - A set of initial states  $I \subseteq State$ 
    - Initial state condition  $I(s) :\Leftrightarrow s \in I$
  - A transition function  $F: State \xrightarrow{partial} State$ .
- A run of a deterministic system  $\langle I, F \rangle$  is a (finite or infinite) sequence  $s_0 \rightarrow s_1 \rightarrow \ldots$  of states such that
  - $s_0 \in I$  (respectively  $I(s_0)$ ).
  - $s_{i+1} = F(s_i)$  (for all sequence indices i)
  - If s ends in a state  $s_n$ , then F is not defined on  $s_n$ .

### Nondeterministic Systems



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In a concurrent system, each component may change its local state, thus the successor state is not uniquely determined.

- The system is nondeterministic.
  - We have a transition relation *R* on states.
  - $R(s_0, s_1)$  means " $s_1$  is a (possible) successor of  $s_0$ .
- $\blacksquare$  Given an initial state  $s_0$ , the execution is not uniquely determined.
  - Both  $s_0 \rightarrow s_1 \rightarrow \dots$  and  $s_0 \rightarrow s_1' \rightarrow \dots$  are possible.
- A non-deterministic system (model) is a pair  $\langle I, R \rangle$ .
  - A set of initial states (initial state condition)  $I \subseteq State$ .
  - A transition relation  $R \subseteq State \times State$ .
- A run s of a nondeterministic system  $\langle I, R \rangle$  is a (finite or infinite) sequence  $s_0 \rightarrow s_1 \rightarrow s_2 \dots$  of states such that
  - $s_0 \in I$  (respectively  $I(s_0)$ ).
  - $R(s_i, s_{i+1})$  (for all sequence indices i).
  - If s ends in a state  $s_n$ , then there is no state t such that  $R(s_n, t)$ .

#### **Derived Notions**



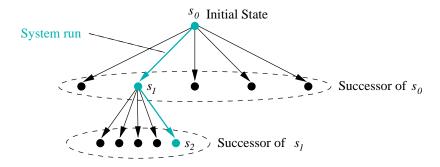
- Successor and predecessor:
  - State t is a (direct) successor of state s, if R(s, t).
  - State *s* is then a predecessor of *t*.
    - A finite run  $s_0 \to \ldots \to s_n$  ends in a state which has no successor.
- Reachability:
  - A state t is reachable, if there exists some run  $s_0 \rightarrow s_1 \rightarrow s_2 \rightarrow \dots$  such that  $t = s_i$  (for some i).
  - A state t is unreachable, if it is not reachable.

Not all states are reachable (typically most are unreachable).

# Reachability Graph



The transitions of a system can be visualized by a graph.



The nodes of the graph are the reachable states of the system.

#### **Examples**



6 1. Automata



Fig. 1.1. A model of a watch

of  $\mathcal{A}_{c3}$  correspond to the possible counter values. Its transitions reflect the possible actions on the counter. In this example we restrict our operations to increments (inc) and decrements (dec).

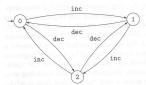


Fig. 1.2. Ac3: a modulo 3 counter

B.Berard et al: "Systems and Software Verification", 2001.

### **Examples**



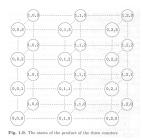
- A deterministic system  $W = (I_W, F_W)$  ("watch").
  - State :=  $\{\langle h, m \rangle : h \in \mathbb{N}_{24} \land m \in \mathbb{N}_{60} \}$ .
  - $I_W(h, m) :\Leftrightarrow h = 0 \land m = 0.$ 
    - $I_W := \{ \langle h, m \rangle : h = 0 \land m = 0 \} = \{ \langle 0, 0 \rangle \}.$
  - $F_W(h, m) :=$  if m < 59 then  $\langle h, m+1 \rangle$  else if h < 24 then  $\langle h+1, 0 \rangle$  else  $\langle 0, 0 \rangle$ .
- A nondeterministic system  $C = (I_C, R_C)$  (modulo 3 "counter").
  - State :=  $\mathbb{N}_3$ .
  - $I_{C}(i) : \Leftrightarrow i = 0.$
  - $R_C(i,i'):\Leftrightarrow inc(i,i')\vee dec(i,i').$ 
    - inc(i, i'):  $\Leftrightarrow$  if i < 2 then i' = i + 1 else i' = 0.
    - dec(i, i'):  $\Leftrightarrow$  if i > 0 then i' = i 1 else i' = 2.

### **Composing Systems**



Compose n components  $S_i$  to a concurrent system S.

- State space  $State := State_0 \times ... \times State_{n-1}$ .
  - State; is the state space of component i.
  - State space is Cartesian product of component state spaces.
  - Size of state space is product of the sizes of the component spaces.
- **Example**: three counters with state spaces  $\mathbb{N}_2$  and  $\mathbb{N}_3$  and  $\mathbb{N}_4$ .



B.Berard et al: "Systems and Software Verification", 2001.

# **Initial States of Composed System**



What are the initial states *I* of the composed system?

- $\blacksquare \mathsf{Set} \ I := I_0 \times \ldots \times I_{n-1}.$ 
  - $I_i$  is the set of initial states of component i.
  - Set of initial states is Cartesian product of the sets of initial states of the individual components.
- Predicate  $I(s_0, \ldots, s_{n-1}) : \Leftrightarrow I_0(s_0) \wedge \ldots \wedge I_{n-1}(s_{n-1})$ .
  - $I_i$  is the initial state condition of component i.
  - Initial state condition is conjunction of the initial state conditions of the components on the corresponding projection of the state.

Size of initial state set is the product of the sizes of the initial state sets of the individual components.

### **Transitions of Composed System**



#### Which transitions can the composed system perform?

- Synchronized composition.
  - At each step, every component must perform a transition.
    - $R_i$  is the transition relation of component i.

$$R(\langle s_0,\ldots,s_{n-1}\rangle,\langle s_0',\ldots,s_{n-1}'\rangle):\Leftrightarrow R_0(s_0,s_0')\wedge\ldots\wedge R_{n-1}(s_{n-1},s_{n-1}').$$

- Asynchronous composition.
  - At each moment, every component may perform a transition.
    - At least one component performs a transition.
    - Multiple simultaneous transitions are possible
    - With *n* components,  $2^n 1$  possibilities of (combined) transitions.

$$R(\langle s_0, \dots, s_{n-1} \rangle, \langle s'_0, \dots, s'_{n-1} \rangle) :\Leftrightarrow (R_0(s_0, s'_0) \wedge \dots \wedge s_{n-1} = s'_{n-1}) \vee \dots \\ (s_0 = s'_0 \wedge \dots \wedge R_{n-1}(s_{n-1}, s'_{n-1})) \vee \dots \\ (R_0(s_0, s'_0) \wedge \dots \wedge R_{n-1}(s_{n-1}, s'_{n-1})).$$

### **Example**



System of three counters with state space  $\mathbb{N}_2$  each.

Synchronous composition:

$$[0,0,0] \leftrightarrows [1,1,1]$$

Asynchronous composition:

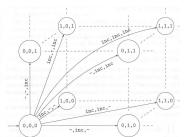


Fig. 1.10. A few transitions of the product of the three counters

B.Berard et al: "Systems and Software Verification", 2001.

### **Interleaving Execution**



Simplified view of asynchronous execution.

- At each moment, only one component performs a transition.
  - Do not allow simultaneous transition  $t_i|t_i$  of two components i and j.
  - Transition sequences  $t_i$ ;  $t_i$  and  $t_i$ ;  $t_i$  are possible.
    - All possible interleavings of component transitions are considered.
    - Nondeterminism is used to simulate concurrency.
    - Essentially no change of system properties.
  - With n components, only n possibilities of a transition.

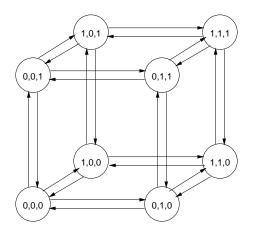
$$R(\langle s_{0}, s_{1}, \dots, s_{n-1} \rangle, \langle s'_{0}, s'_{1}, \dots, s'_{n-1} \rangle) : \Leftrightarrow (R_{0}(s_{0}, s'_{0}) \wedge s_{1} = s'_{1} \wedge \dots \wedge s_{n-1} = s'_{n-1}) \vee (s_{0} = s'_{0} \wedge R_{1}(s_{1}, s'_{1}) \wedge \dots \wedge s_{n-1} = s'_{n-1}) \vee \dots (s_{0} = s'_{0} \wedge s_{1} = s'_{1} \wedge \dots \wedge R_{n-1}(s_{n-1}, s'_{n-1})).$$

Interleaving model (respectively a variant of it) suffices in practice.

### **Example**



System of three counters with state space  $\mathbb{N}_2$  each.



## **Digital Circuits**



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#### Synchronous composition of hardware components.

■ A modulo 8 counter  $C = \langle I_C, R_C \rangle$ .

State := 
$$\mathbb{N}_2 \times \mathbb{N}_2 \times \mathbb{N}_2$$
.

$$I_C(v_0, v_1, v_2) :\Leftrightarrow v_0 = v_1 = v_2 = 0.$$

$$R_{C}(\langle v_{0}, v_{1}, v_{2} \rangle, \langle v'_{0}, v'_{1}, v'_{2} \rangle) :\Leftrightarrow R_{0}(v_{0}, v'_{0}) \wedge R_{1}(v_{0}, v_{1}, v'_{1}) \wedge R_{2}(v_{0}, v_{1}, v_{2}, v'_{2}).$$

$$R_0(v_0, v_0') :\Leftrightarrow v_0' = \neg v_0.$$

$$R_1(v_0, v_1, v_1') :\Leftrightarrow v_1' = v_0 \oplus v_1.$$

$$R_2(v_0, v_1, v_2, v_2') :\Leftrightarrow v_2' = \neg (v_0 \land v_1) \oplus v_2.$$

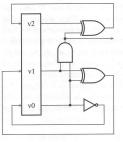


Figure 2.1 Synchronous modulo 8 counter.

Edmund Clarke et al: "Model Checking", 1999.

#### **Concurrent Software**



Asynchronous composition of software components with shared variables.

■ A mutual exclusion program  $M = \langle I_M, R_M \rangle$ .

```
State := PC \times PC \times \mathbb{N}_2. \ // \ shared \ variable
I_M(p, q, turn) :\Leftrightarrow p = I_0 \wedge q = I_1.
R_M(\langle p, q, turn \rangle, \langle p', q', turn' \rangle) :\Leftrightarrow \\ (P(\langle p, turn \rangle, \langle p', turn' \rangle) \wedge q' = q) \vee (Q(\langle q, turn \rangle, \langle q', turn' \rangle) \wedge p' = p).
P(\langle p, turn \rangle, \langle p', turn' \rangle) :\Leftrightarrow \\ (p = I_0 \wedge p' = NC_0 \wedge turn' = turn) \vee \\ (p = NC_0 \wedge p' = CR_0 \wedge turn' = 1).
Q(\langle q, turn \rangle, \langle q', turn' \rangle) :\Leftrightarrow \\ (q = CR_0 \wedge p' = I_0 \wedge turn' = turn) \vee \\ (q = NC_1 \wedge q' = NC_1 \wedge turn' = turn) \vee \\ (q = CR_1 \wedge q' = I_1 \wedge turn' = 0).
```

#### **Concurrent Software**



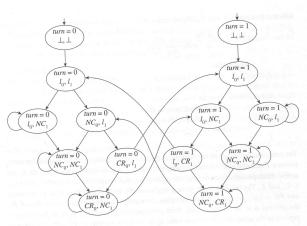


Figure 2.2
Reachable states of Kripke structure for mutual exclusion example.

Edmund Clarke et al: "Model Checking", 1999.

#### Model guarantees mutual exclusion.

### **Modeling Commands**



Transition relations are typically described in a particular form.

- $\blacksquare R(s,s') :\Leftrightarrow P(s) \land s' = F(s).$ 
  - Precondition P on state in which transition can be performed.
    - If P(s) holds, then there exists some s' such that R(s, s').
  - Transition function *F* that determines the successor of *s*.
    - F is defined for all states for which s holds:  $F: \{s \in State : P(s)\}$  → State.
- Examples:
  - Assignment: x := e.
    - $R(\langle x, y \rangle, \langle x', y' \rangle) : \Leftrightarrow \mathsf{true} \wedge (x' = e \wedge y' = y).$
  - Wait statement: wait P(x, y).
    - $\blacksquare R(\langle x, y \rangle, \langle x', y' \rangle) : \Leftrightarrow P(x, y) \land (x' = x \land y' = y).$
  - Guarded assignment:  $P(x, y) \rightarrow x := e$ .
    - $P(\langle x, y \rangle, \langle x', y' \rangle) : \Leftrightarrow P(x, y) \wedge (x' = e \wedge y' = y).$

Most programming language commands can be translated into this form.

# Message Passing Systems



How to model an asynchronous system without shared variables where the components communicate/synchronize by exchanging messages?

- Given a label set  $Label = Int \cup Ext \cup \overline{Ext}$ .
  - Disjoint sets *Int* and *Ext* of internal and external labels.
    - "Anonymous" label \_ ∈ Int.
  - Complementary label set  $\overline{L} := {\overline{I} : I \in L}$ .
- A labeled system is a pair  $\langle I, R \rangle$ .
  - Initial state condition  $I \subseteq State \times State$ .
  - Labeled transition relation  $R \subseteq Label \times State \times State$ .
- A run of a labeled system  $\langle I, R \rangle$  is a (finite or infinite) sequence  $s_0 \xrightarrow{l_0} s_1 \xrightarrow{l_1} \dots$  of states such that
  - $s_0 \in I$ .
  - $R(I_i, s_i, s_{i+1})$  (for all sequence indices i).
  - If s ends in a state  $s_n$ , there is no label I and state t s.t.  $R(I, s_n, t)$ .

# Synchronization by Message Passing



Compose a set of *n* labeled systems  $\langle I_i, R_i \rangle$  to a system  $\langle I, R \rangle$ .

- State space  $State := State_0 \times ... \times State_{n-1}$ .
- Initial states  $I := I_0 \times ... \times I_{n-1}$ .
  - $I(s_0,\ldots,s_{n-1}):\Leftrightarrow I_0(s_0)\wedge\ldots\wedge I_{n-1}(s_{n-1}).$
- Transition relation

$$R(I, \langle s_i \rangle_{i \in \mathbb{N}_n}, \langle s_i' \rangle_{i \in \mathbb{N}_n}) \Leftrightarrow (I \in Int \land \exists i \in \mathbb{N}_n : R_i(I, s_i, s_i') \land \forall k \in \mathbb{N}_n \backslash \{i\} : s_k = s_k') \lor (I = \_ \land \exists I \in Ext, i \in \mathbb{N}_n, j \in \mathbb{N}_n : R_i(I, s_i, s_i') \land R_j(\overline{I}, s_j, s_i') \land \forall k \in \mathbb{N}_n \backslash \{i, j\} : s_k = s_k').$$

Either a component performs an internal transition or two components simultaneously perform an external transition with complementary labels.

### **Example**



```
\begin{array}{lll} 0 :: \ \textbf{loop} & & & 1 :: \ \textbf{loop} \\ & a_0 : \ \textbf{send(i)} & & b_0 : j := \ \textbf{receive()} \\ & a_1 : i := \ \textbf{receive()} & || & b_1 : j := j + 1 \\ & a_2 : i := i + 1 & b_2 : \ \textbf{send(j)} \\ & \textbf{end} & & \textbf{end} \end{array}
```

Two labeled systems  $\langle I_0, R_0 \rangle$  and  $\langle I_1, R_1 \rangle$ .  $State_0 = State_1 = PC \times \mathbb{N}$ ,  $Internal := \{A, B\}$ ,  $External := \{M, N\}$ .  $I_0(p, i) :\Leftrightarrow p = a_0 \land i \in \mathbb{N}$ ;  $I_1(q, j) :\Leftrightarrow q = b_0$ .  $R_0(I, \langle p, i \rangle, \langle p', i' \rangle) :\Leftrightarrow$   $(I = \overline{M} \land p = a_0 \land p' = a_1 \land i' = i) \lor$   $(I = N \land p = a_1 \land p' = a_2 \land i' = j) \lor // \text{ illegal!}$   $(I = A \land p = a_2 \land p' = a_0 \land i' = i + 1)$ .  $R_1(I, \langle q, j \rangle, \langle q', j' \rangle) :\Leftrightarrow$   $(I = M \land q = b_0 \land q' = b_1 \land j' = i) \lor // \text{ illegal!}$   $(I = B \land q = b_1 \land q' = b_2 \land j' = j + 1) \lor$  $(I = \overline{N} \land a = b_2 \land a' = b_0 \land i' = i)$ .

# **Example (Continued)**



Composition of  $\langle I_0, R_0 \rangle$  and  $\langle I_1, R_1 \rangle$  to  $\langle I, R \rangle$ .

$$State = (PC \times \mathbb{N}) \times (PC \times \mathbb{N}).$$

$$I(p, i, q, j) :\Leftrightarrow p = a_0 \wedge i \in \mathbb{N} \wedge q = b_0.$$

$$R(I, \langle p, i, q, j \rangle, \langle p', i', q', j' \rangle) :\Leftrightarrow (I = A \wedge (p = a_2 \wedge p' = a_0 \wedge i' = i + 1) \wedge (q' = q \wedge j' = j)) \vee (I = B \wedge (p' = p \wedge i' = i) \wedge (q = b_1 \wedge q' = b_2 \wedge j' = j + 1)) \vee (I = A \wedge (p = a_0 \wedge p' = a_1 \wedge i' = i) \wedge (q = b_0 \wedge q' = b_1 \wedge j' = i)) \vee (I = A \wedge (p = a_1 \wedge p' = a_2 \wedge j' = j) \wedge (q = b_2 \wedge q' = b_0 \wedge j' = j)).$$

Problem: state relation of each component refers to local variable of other component (variables are shared).

# **Example (Revised)**



```
\begin{array}{lll} 0 :: \ \textbf{loop} & & 1 :: \ \textbf{loop} \\ & a_0 : \ \textbf{send(i)} & & b_0 : j := \ \textbf{receive()} \\ & a_1 : i := \ \textbf{receive()} & || & b_1 : j := j + 1 \\ & a_2 : i := i + 1 & b_2 : \ \textbf{send(j)} \\ & \textbf{end} & & \textbf{end} \end{array}
```

■ Two labeled systems  $\langle I_0, R_0 \rangle$  and  $\langle I_1, R_1 \rangle$ .

```
External := \{M_k : k \in \mathbb{N}\} \cup \{N_k : k \in \mathbb{N}\}.
R_0(I, \langle p, i \rangle, \langle p', i' \rangle) :\Leftrightarrow
(I = \overline{M_i} \land p = a_0 \land p' = a_1 \land i' = i) \lor
(\exists k \in \mathbb{N} : I = N_k \land p = a_1 \land p' = a_2 \land i' = k) \lor
(I = A \land p = a_2 \land p' = a_0 \land i' = i + 1).
R_1(I, \langle q, j \rangle, \langle q', j' \rangle) :\Leftrightarrow
(\exists k \in \mathbb{N} : I = M_k \land q = b_0 \land q' = b_1 \land j' = k) \lor
(I = B \land q = b_1 \land q' = b_2 \land j' = j + 1) \lor
(I = \overline{N_j} \land q = b_2 \land q' = b_0 \land j' = j).
```

#### Encode message value in label.

# **Example (Continued)**



Composition of  $\langle I_0, R_0 \rangle$  and  $\langle I_1, R_1 \rangle$  to  $\langle I, R \rangle$ .

$$State = (PC \times \mathbb{N}) \times (PC \times \mathbb{N}).$$

$$I(p, i, q, j) :\Leftrightarrow p = a_0 \wedge i \in \mathbb{N} \wedge q = b_0.$$

$$R(I, \langle p, i, q, j \rangle, \langle p', i', q', j' \rangle) :\Leftrightarrow (I = A \wedge (p = a_2 \wedge p' = a_0 \wedge i' = i + 1) \wedge (q' = q \wedge j' = j)) \vee (I = B \wedge (p' = p \wedge i' = i) \wedge (q = b_1 \wedge q' = b_2 \wedge j' = j + 1)) \vee (I = A \wedge B \in \mathbb{N} : k = i \wedge (p = a_0 \wedge p' = a_1 \wedge i' = i) \wedge (q = b_0 \wedge q' = b_1 \wedge j' = k)) \vee (I = A \wedge B \in \mathbb{N} : k = j \wedge (p = a_1 \wedge p' = a_2 \wedge i' = k) \wedge (q = b_2 \wedge q' = b_0 \wedge i' = j)).$$

Logically equivalent to previous definition of transition relation.



1. Checking a Client/Server System with SPIN

2. Modeling Concurrent Systems

3. A Model of the Client/Server System

#### Basic Idea



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#### Asynchronous composition of three components Client<sub>1</sub>, Client<sub>2</sub>, Server.

- Client<sub>i</sub>: State :=  $PC \times \mathbb{N}_2 \times \mathbb{N}_2$ .
  - Three variables pc, request, answer.
  - pc represents the program counter.
  - request is the buffer for outgoing requests.
    - Filled by client, when a request is to be sent to server.
  - answer is the buffer for incoming answers.
    - Checked by client, when it waits for an answer from the server.
- Server: State :=  $(\mathbb{N}_3)^3 \times (\{1,2\} \rightarrow \mathbb{N}_2)^2$ .
  - Variables given, waiting, sender, rbuffer, sbuffer.
  - No program counter.
    - We use the value of *sender* to check whether server waits for a request (sender = 0) or answers a request ( $sender \neq 0$ ).
  - Variables given, waiting, sender as in program.
  - rbuffer(i) is the buffer for incoming requests from client i.
  - $\blacksquare$  sbuffer(i) is the buffer for outgoing answers to client i.

#### **External Transitions**



- $Ext := \{REQ_1, REQ_2, ANS_1, ANS_2\}.$ 
  - Transition labeled *REQ<sub>i</sub>* transmits a request from client *i* to server.
    - Enabled when  $request \neq 0$  in client i.
    - Effect in client *i*: request' = 0.
    - Effect in server: rbuffer'(i) = 1.
  - Transition labeled ANS; transmits an answer from server to client i
    - Enabled when sbuffer(i)  $\neq$  0.
    - Effect in server: sbuffer'(i) = 0.
    - Effect in client i: answer' = 1.

The external transitions correspond to system-level actions of the communication subsystem (rather than to the user-level actions of the client/server program).

#### The Client



```
Client system C_i = \langle IC_i, RC_i \rangle.
State := PC \times \mathbb{N}_2 \times \mathbb{N}_2.
Int := \{R_i, S_i, C_i\}.
IC_i(pc, request, answer) :\Leftrightarrow
   pc = R \land request = 0 \land answer = 0.
RC_i(I, \langle pc, request, answer \rangle,
      \langle pc', request', answer' \rangle) :\Leftrightarrow
   (I = R_i \land pc = R \land request = 0 \land
      pc' = S \land request' = 1 \land answer' = answer) \lor
   (I = S_i \land pc = S \land answer \neq 0 \land
      pc' = C \land request' = request \land answer' = 0) \lor
   (I = C_i \land pc = C \land request = 0 \land
      pc' = R \land request' = 1 \land answer' = answer) \lor
   (I = \overline{REQ}_i \land request \neq 0 \land
```

 $pc' = pc \land request' = 0 \land answer' = answer) \lor$ 

 $pc' = pc \land request' = request \land answer' = 1$ ).

```
Client(ident):
   param ident
begin
   loop
   ...
R: sendRequest()
S: receiveAnswer()
C: // critical region
   ...
   sendRequest()
endloop
end Client
```

 $(I = ANS: \land$ 

#### The Server



```
Server:
Server system S = \langle IS, RS \rangle.
                                                                           local given, waiting, sender
State := (\mathbb{N}_3)^3 \times (\{1,2\} \to \mathbb{N}_2)^2.
                                                                        begin
Int := \{D1, D2, F, A1, A2, W\}.
                                                                           given := 0; waiting := 0
                                                                           loop
IS(given, waiting, sender, rbuffer, sbuffer) : \Leftrightarrow
                                                                              sender := receiveRequest()
  given = waiting = sender = 0 \land
                                                                              if sender = given then
   rbuffer(1) = rbuffer(2) = sbuffer(1) = sbuffer(2) = 0.
                                                                                 if waiting = 0 then
                                                                        F:
                                                                                    given := 0
RS(I, \langle given, waiting, sender, rbuffer, sbuffer \rangle,
                                                                                 else
      \langle given', waiting', sender', rbuffer', sbuffer' \rangle : \Leftrightarrow
                                                                        A1:
                                                                                    given := waiting;
  \exists i \in \{1,2\}:
                                                                                    waiting := 0
     (I = D_i \land sender = 0 \land rbuffer(i) \neq 0 \land
                                                                                    sendAnswer(given)
     sender' = i \land rbuffer'(i) = 0 \land
                                                                                 endif
     U(given, waiting, sbuffer) \land
                                                                              elsif given = 0 then
     \forall i \in \{1,2\} \setminus \{i\} : U_i(rbuffer)) \vee
                                                                        A2:
                                                                                 given := sender
                                                                                 sendAnswer(given)
                                                                              else
U(x_1,\ldots,x_n):\Leftrightarrow x_1'=x_1\wedge\ldots\wedge x_n'=x_n.
                                                                        W:
                                                                                 waiting := sender
U_i(x_1,\ldots,x_n):\Leftrightarrow x_1'(j)=x_1(j)\wedge\ldots\wedge x_n'(j)=x_n(j).
                                                                              endif
                                                                           endloop
```

end Server

## The Server (Contd)



```
Server:
                                                                                                                                                                                                                                                                                                                                                                                                                                                   local given, waiting, sender
                                                                                                                                                                                                                                                                                                                                                                                                                                 begin
(I = F \land sender \neq 0 \land sender = given \land waiting = 0 \land
                                                                                                                                                                                                                                                                                                                                                                                                                                                   given := 0; waiting := 0
                given' = 0 \land sender' = 0 \land
                                                                                                                                                                                                                                                                                                                                                                                                                                                   loop
                   U(waiting, rbuffer, sbuffer)) \lor
                                                                                                                                                                                                                                                                                                                                                                                                                                 D: sender := receiveRequest()
                                                                                                                                                                                                                                                                                                                                                                                                                                                                    if sender = given then
 (I = A1 \land sender \neq 0 \land sbuffer(waiting) = 0 \land
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      if waiting = 0 then
                  sender = given \land waiting \neq 0 \land
                                                                                                                                                                                                                                                                                                                                                                                                                                 F:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     given := 0
                  given' = waiting \land waiting' = 0 \land
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     else
                  sbuffer'(waiting) = 1 \land sender' = 0 \land
                                                                                                                                                                                                                                                                                                                                                                                                                                 A1:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     given := waiting;
                  U(rbuffer) \land
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       waiting := 0
                \forall j \in \{1,2\} \setminus \{waiting\} : U_i(sbuffer) \setminus \forall j \in \{1,2\} \setminus \{waiting\} : U_i(sbuffer) : 
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       sendAnswer(given)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     endif
 (I = A2 \land sender \neq 0 \land sbuffer(sender) = 0 \land
                                                                                                                                                                                                                                                                                                                                                                                                                                                                    elsif given = 0 then
                  sender \neq given \wedge given = 0 \wedge
                                                                                                                                                                                                                                                                                                                                                                                                                                 A2:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     given := sender
                given' = sender \land
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      sendAnswer(given)
                  sbuffer'(sender) = 1 \land sender' = 0 \land
                                                                                                                                                                                                                                                                                                                                                                                                                                                                    else
                  U(waiting, rbuffer) \land
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     waiting := sender
                \forall i \in \{1,2\} \setminus \{sender\} : U_i(sbuffer) \setminus \forall i \in \{1,2\} \setminus \{sender\} : U_i(sbuffer) \setminus \{sender\} \in U_i
                                                                                                                                                                                                                                                                                                                                                                                                                                                                    endif
                                                                                                                                                                                                                                                                                                                                                                                                                                                   endloop
```

end Server

# The Server (Contd'2)



```
(I = W \land sender \neq 0 \land sender \neq given \land given \neq 0 \land
   waiting' := sender \land sender' = 0 \land
  U(given, rbuffer, sbuffer)) \lor
\exists i \in \{1, 2\}:
   (I = REQ_i \land rbuffer'(i) = 1 \land
       U(given, waiting, sender, sbuffer) \land
      \forall j \in \{1,2\} \setminus \{i\} : U_i(rbuffer)) \vee
   (I = \overline{ANS_i} \land sbuffer(i) \neq 0 \land
      sbuffer'(i) = 0 \land
       U(given, waiting, sender, rbuffer) \land
      \forall j \in \{1,2\} \setminus \{i\} : U_i(sbuffer)).
```

```
Server:
  local given, waiting, sender
begin
  given := 0; waiting := 0
  loop
    sender := receiveRequest()
    if sender = given then
      if waiting = 0 then
F:
        given := 0
      else
A1:
        given := waiting;
        waiting := 0
        sendAnswer(given)
      endif
    elsif given = 0 then
A2:
      given := sender
      sendAnswer(given)
    else
W:
      waiting := sender
    endif
  endloop
```

end Server

#### **Communication Channels**



We also model the communication medium between components.



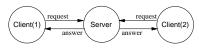
- Bounded channel Channel<sub>i,j</sub> = (ICH, RCH).
  - Transfers message from component with address i to component j.
    - May hold at most N messages at a time (for some N).
  - State :=  $\langle Value \rangle$ .
    - Sequence of values of type Value.
  - $Ext := \{SEND_{i,j}(m) : m \in Value\} \cup \{RECEIVE_{i,j}(m) : m \in Value\}.$ 
    - By  $SEND_{i,j}(m)$ , channel receives from sender i a message m destined for receiver j; by  $RECEIVE_{i,j}(m)$ , channel forwards that message.

```
\begin{split} & \textit{ICH}(\textit{queue}) : \Leftrightarrow \textit{queue} = \langle \rangle. \\ & \textit{RCH}(\textit{I}, \textit{queue}, \textit{queue'}) : \Leftrightarrow \\ & \exists \textit{i} \in \textit{Address}, \textit{j} \in \textit{Address}, \textit{m} \in \textit{Value} : \\ & (\textit{I} = \textit{SEND}_{\textit{i},\textit{j}}(\textit{m}) \land |\textit{queue}| < \textit{N} \land \textit{queue'} = \textit{queue} \circ \langle \textit{m} \rangle) \lor \\ & (\textit{I} = \overline{\textit{RECEIVE}}_{\textit{i},\textit{j}}(\textit{m}) \land |\textit{queue}| > 0 \land \textit{queue} = \langle \textit{m} \rangle \circ \textit{queue'}). \end{split}
```

# **Client/Server Example with Channels**



- Server receives address 0.
  - Label  $REQ_i$  is renamed to  $RECEIVE_{i,0}(R)$ .
  - Label  $\overline{ANS_i}$  is renamed to  $\overline{SEND_{0,i}(A)}$ .
- Client *i* receives address i ( $i \in \{1,2\}$ ).
  - Label  $\overline{REQ_i}$  is renamed to  $\overline{SEND_{i,0}(R)}$ .
  - Label  $ANS_i$  is renamed to  $RECEIVE_{0,i}(A)$ .
- System is composed of seven components:
  - Server, Client<sub>1</sub>, Client<sub>2</sub>.
  - Channel<sub>0,1</sub>, Channel<sub>1,0</sub>.
  - Channel<sub>0,2</sub>, Channel<sub>2,0</sub>.



Also channels are active system components.

# **Summary**



We have now seen a model of a client/server system (as used by SPIN).

- A system is described by
  - its (finite or infinite) state space,
  - the initial state condition (set of input states),
  - the transition relation on states.
- State space of composed system is product of component spaces.
  - Variable shared among components occurs only once in product.
- System composition can be
  - synchronous: conjunction of individual transition relations.
    - Suitable for digital hardware.
  - asynchronous: disjunction of relations.
    - Interleaving model: each relation conjoins the transition relation of one component with the identity relations of all other components.
    - Suitable for concurrent software.
- **Labels** may be introduced for synchronization/communication.
  - Simultaneous transition of two components.
  - Label may describe value to be communicated.