Introduction to Parallel Computation

Wolfgang Schreiner
Research Institute for Symbolic Computation (RISC-Linz)
Johannes Kepler University, A-4040 Linz, Austria

Wolfgang.Schreiner@risc.uni-linz.ac.at
http://www.risc.uni-linz.ac.at/people/schreine

A Graph-Theoretical Problem

"All Pairs Shortest Paths"

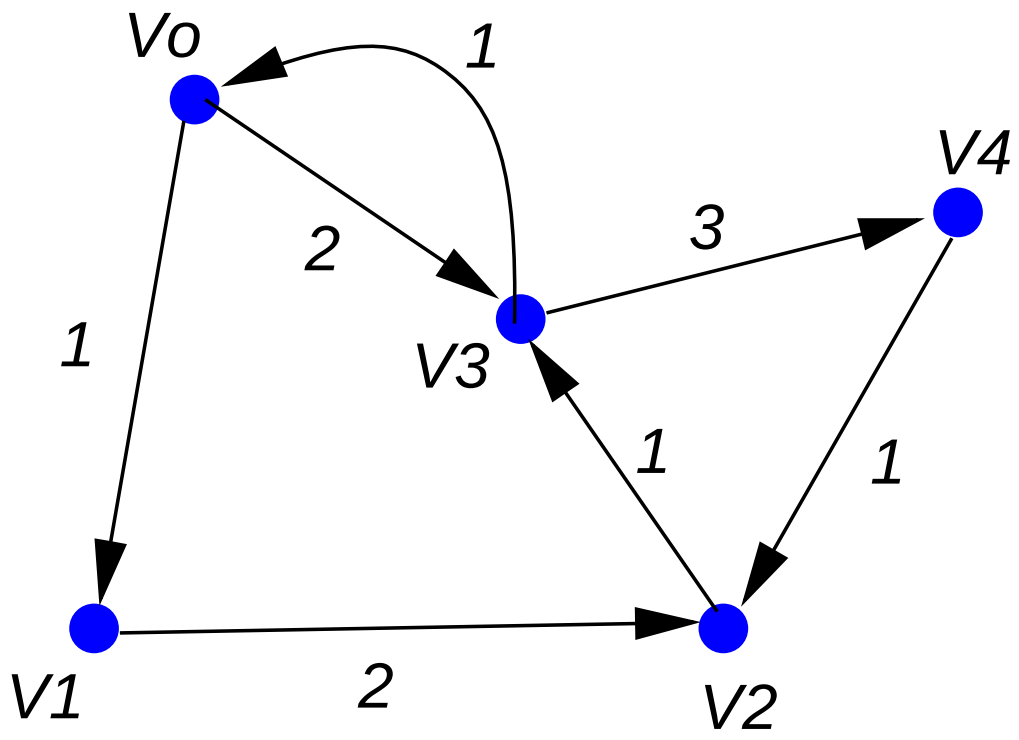
- Given: graph $G = (V, A)$
  - Directed acyclic graph, $|V| = n$
- Representation: weight matrix W

$$W = \begin{cases} 0 & \text{if } i = j \\ w > 0 & \text{if edge of length } w \\ & \text{between nodes } i \text{ and } j \\ \infty & \text{if no edge between } i \text{ and } j \end{cases}$$

- Wanted: distance matrix D

$$D = \begin{cases} 0 & \text{if } i = j \\ d > 0 & \text{if } i \neq j \text{ where } d \text{ length of} \\ & \text{shortest path between } i \text{ and } j \\ \infty & \text{if no edge between } i \text{ and } j \end{cases}$$

Example



W	v_0	v_1	v_2	v_3	v_4
v_0	0	1	∞	2	∞
v_1	∞	0	2	∞	∞
v_2	∞	∞	0	1	∞
v_3	1	∞	∞	0	3
v_4	∞	∞	1	∞	0

$\Rightarrow$

D	v_0	v_1	v_2	v_3	v_4
v_0	0	1	3	2	4
v_1	4	0	2	3	3
v_2	2	3	0	1	4
v_3	1	2	4	0	3
v_4	3	4	1	2	0

Solution Idea

- Construct sequence of matrices
  - $D_0, D_1, \ldots, D_{n-1}$
  - D_i describes all shortest paths with not more than i edges.
- Consequence: $D_{n-1} = D$
- Proof
  - Assume shortest path p with more than $n-1$ edges. Then there is some node v twice in this path i.e. $p = \langle i, \ldots, \underline{v, \ldots, v}, \ldots, j \rangle$. But then path $p' = \langle i, \ldots, v, \ldots, j \rangle$ is shorter!

Construction

$$D_0(i,j) = \begin{cases} 0 & \text{if } i = j \\ \infty & \text{if } i \neq j \end{cases}$$
$$D_1(i,j) = W(i,j)$$

$$D_{n+1}(i,j) = ?$$

Two Cases

1. $|p| = n$
   - $p = \langle \underbrace{i, \ldots, j}_{n \text{ edges}} \rangle$

     $D_{n+1}(i,j) = D_n(i,j)$
2. $|p| = n + 1$
   - $p = \langle \underbrace{i, \ldots, k}_{n \text{ edges}}, j \rangle$

     $D_{n+1}(i,j) = D_n(i,k) + W(k,j)$

$$D_{n+1}(i,j) = \min\{D_n(i,j), \min_k\{D_n(i,k) + W(k,j)\}\}$$

Sequential Algorithm

```
AllPairsShortestPaths(W):
  D = W
  for i=1 to n-1 do
    D = MatMin(D, W)
  return D

MatMin(D, W):
  for i=1 to n do
    for j=1 to n do
      E[i,j] = D[i,j]
      for k=1 to n do
        E[i,j] = min(E[i,j],D[i,k]+W[k,j])
  return E
```

Observation

`MatMin` has same structure as matrix multiplication ($+ \rightarrow$ min, $* \rightarrow +$).

- Define $D \times W = \texttt{MatMult}(D, W)$
- Begin: $D_1 = W$
- General: $D_i = D^{i-1} \times W = W^i$
- End: $D = D_{n-1} = W^{n-1}$

Problem solution is essentially repeated matrix multiplication!

Optimization

- Instead of computing
  - $W^1, W^2, W^3, \ldots, W^{n-1}$
- we can compute
  - $W^1, W^2, W^4, \ldots, W^{2^s}$
    (where $2^s \geq n - 1$).

```
AllPairsShortestPaths(W):
  D = W
  for i=1 to s do
    D = MatMin(D, D)
  return D
```

We can reduce n matrix multiplications to $\log n$ square computations!

Time Analysis

- n nodes.
- log n square computations.
- n^3 (min,+) operations for each square computation.

Sequential complexity $O(\log\ n * n^3)$

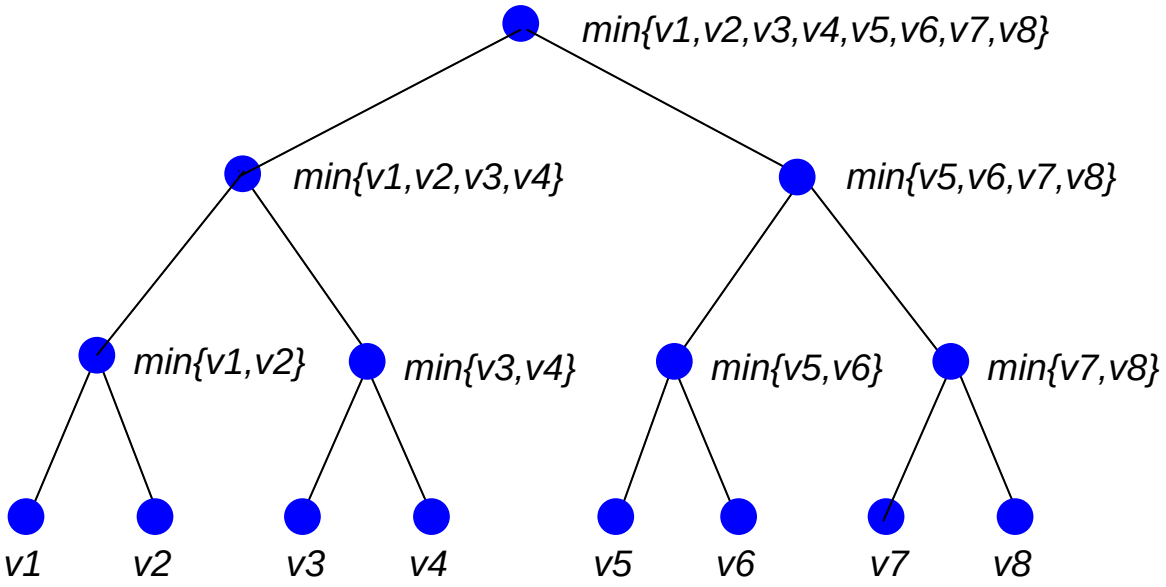
Parallel Algorithm

- Sequence of square operations.
- Each square op. contains n^3 independent (min,+) operations that can be performed in parallel yielding n^3 results.
- Each of the n^2 entries $D(i,j)$ is a minimum of n values.
- Complexity:
  - log n square computations.
  - 1 time step for all (min,+) operations.
  - log n time to compute each of the n^2 minimums.

Parallel complexity $O(\log^2 n)$

Minimum of n Values

Tree-like minimum construction



Depth of tree = computation time = $O(\log n)$

Comparison

- General
  - Sequential: $O(\log n * n^3)$
  - Parallel: $O(\log^2 n)$
  - Processors: $O(n^3)$
- Time/processor product
  - Sequential: $O(\log\ n * n^3)$
  - Parallel: $O(\log^2 n * n^3)$

Parallel algorithm is in some sense less efficient than sequential one!

Parallel Machine Models

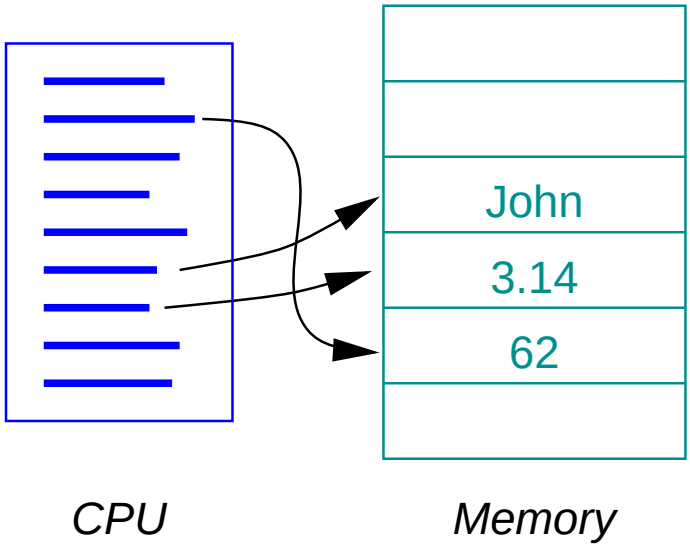
- How to define algorithm?
- How to implement algorithm?
- How to analyze algorithm?

We need a parallel machine and programming model!

Sequential Machine Model

Von Neumann Computer, Random Access Machine (RAM).

- Central Processing Unit (CPU)
  - Executes a stored program.
- Storage Unit (Memory)
  - Random access to any memory cell.

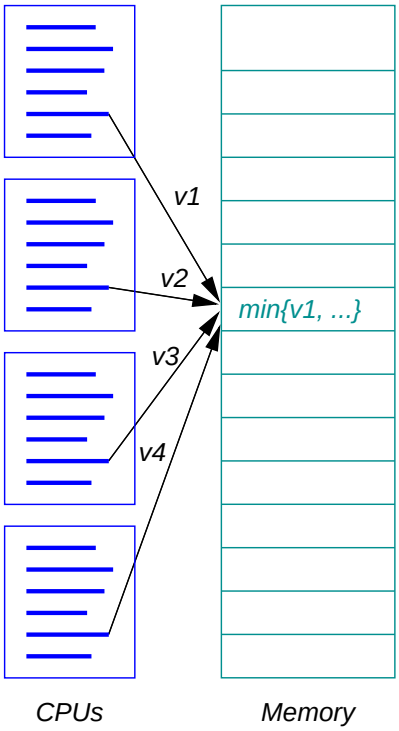


Program execution is sequence of read/write operations on memory.

PRAM Model

Parallel Random Access Machine.

- Set of CPUs.
- CPUs operate on same memory.
- CPUs execute same program lock-step.



Theoretical model for designing and analyzing parallel algorithms.

PRAM Variants

Restrictions of access to memory cells.

- EREW (exclusive read, exclusive write)
  - Only one access to individual memory cell at a time.
- CREW (concurrent read, exclusive write).
  - Multiple concurrent reads to a memory cell, but writes are exclusive.
- CRCW (concurrent read, concurrent write).
  - Multiple concurrent writes allowed, random value (maximum value, sum, ...) is written.

Different variants may yield different complexities of parallel algorithms.

PRAM Program

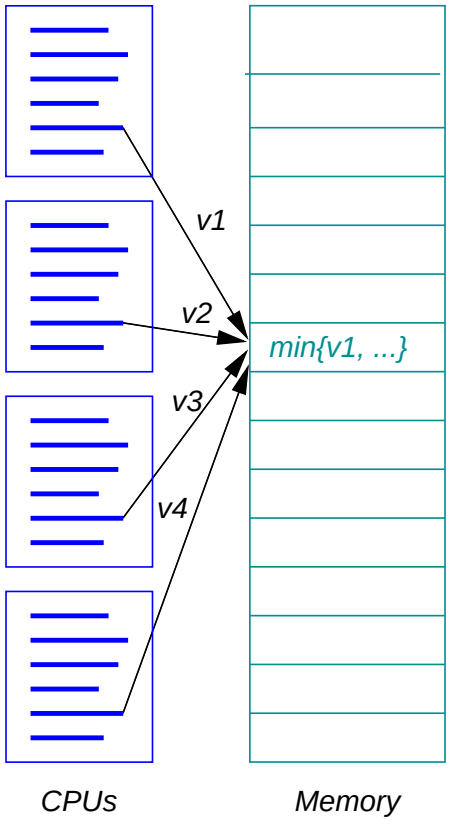
```
AllPairsShortestPaths(W):
  D = W
  for i=1 to s do
    D = MatMin(D, D)
  return D

MatMin(D, W):
  for i=1 to n do in parallel
    for j=1 to n do in parallel
      for k=1 to n do in parallel
        M[i,j,k] =
          min(D[i,j], D[i,k]+W[k,j])
      E[i,j] = MIN(M[i,j])
  return E
```

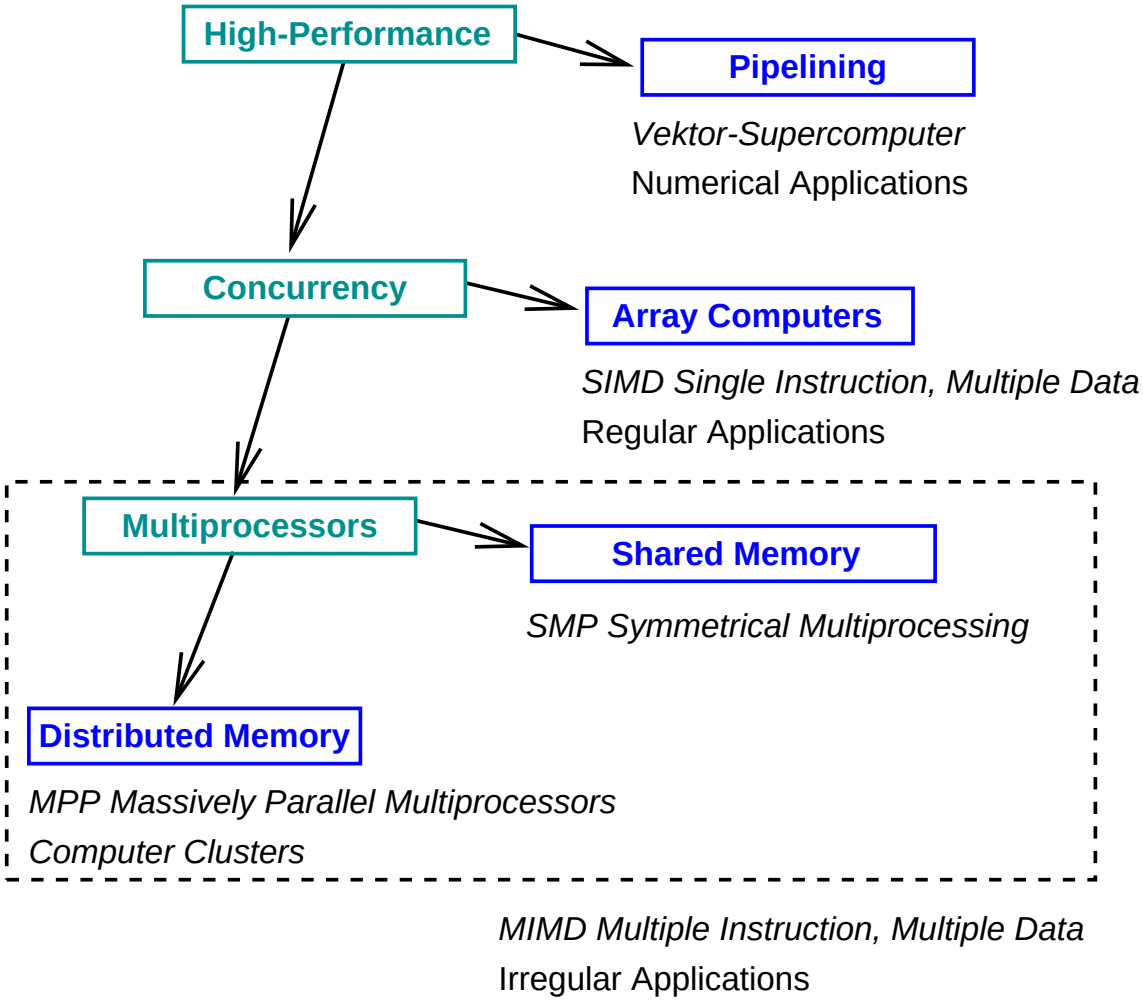
MIN computes minimum of n values.

Complexity of MIN

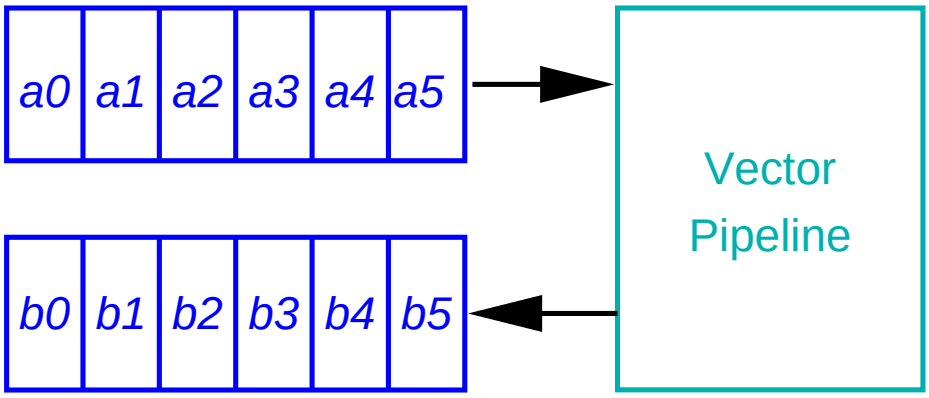
- EREW, CREW: $O(\log n)$
  - Tree-like combination of values.
- CRCW: $O(1)$
  - All values written simultaneously into same cell, minimum value remains.



High-Performance Architectures

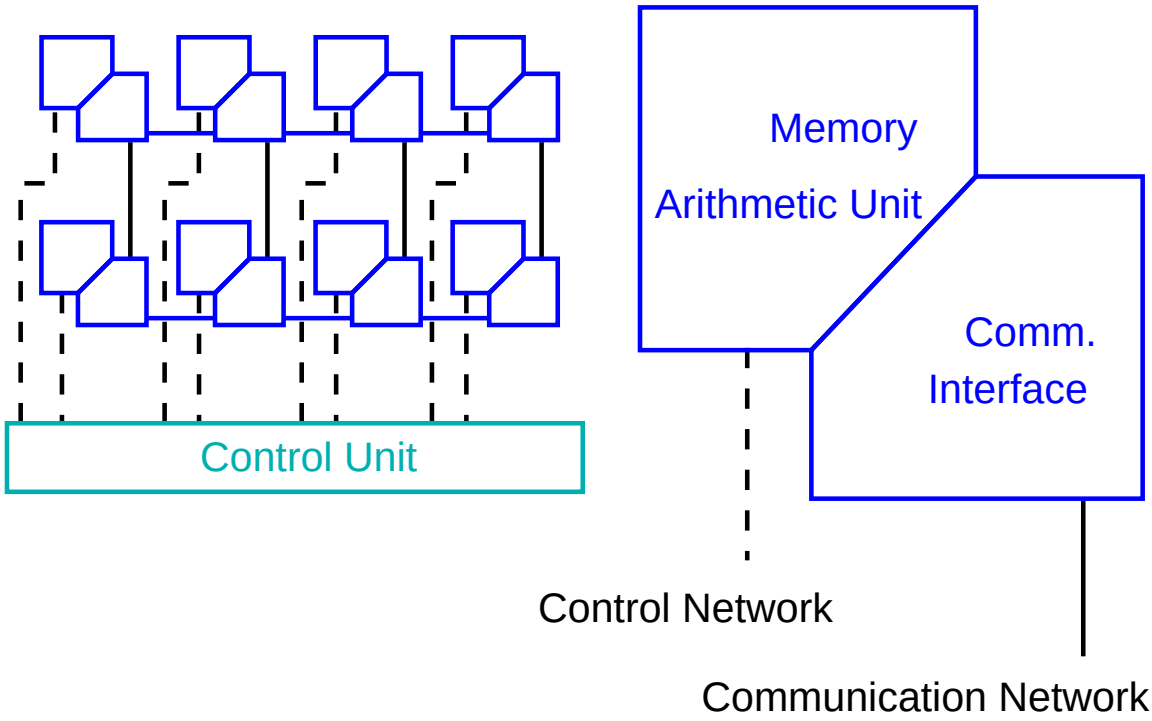


Vector Supercomputers



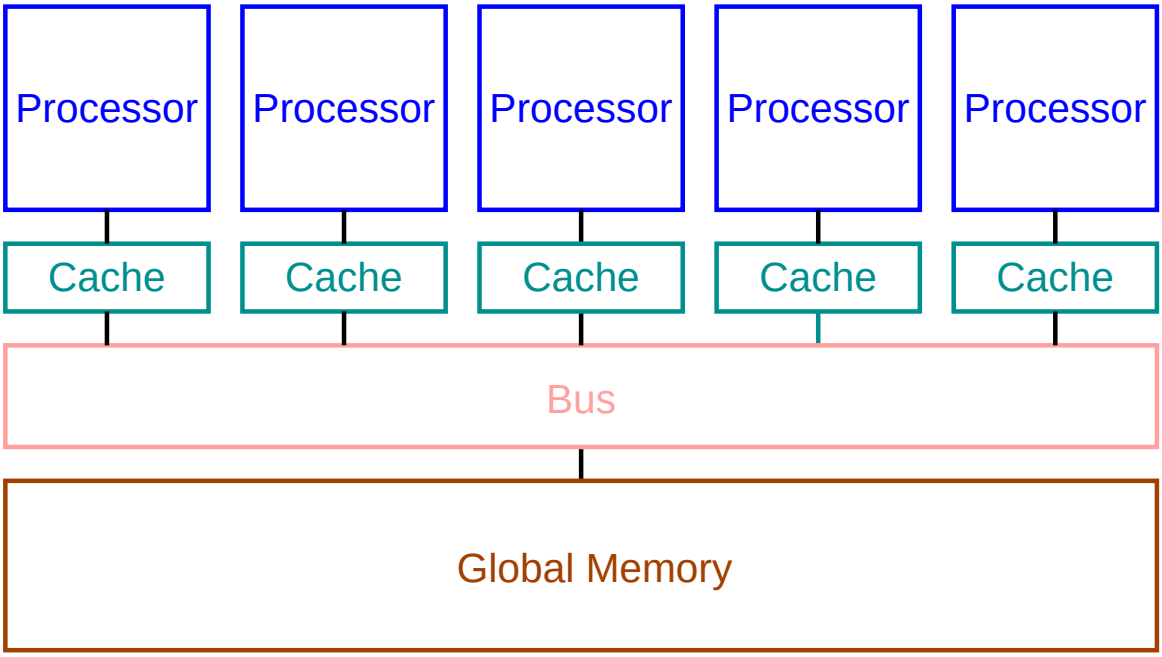
Vectors of numbers are passed through arithmetic pipeline.

SIMD Array Computers



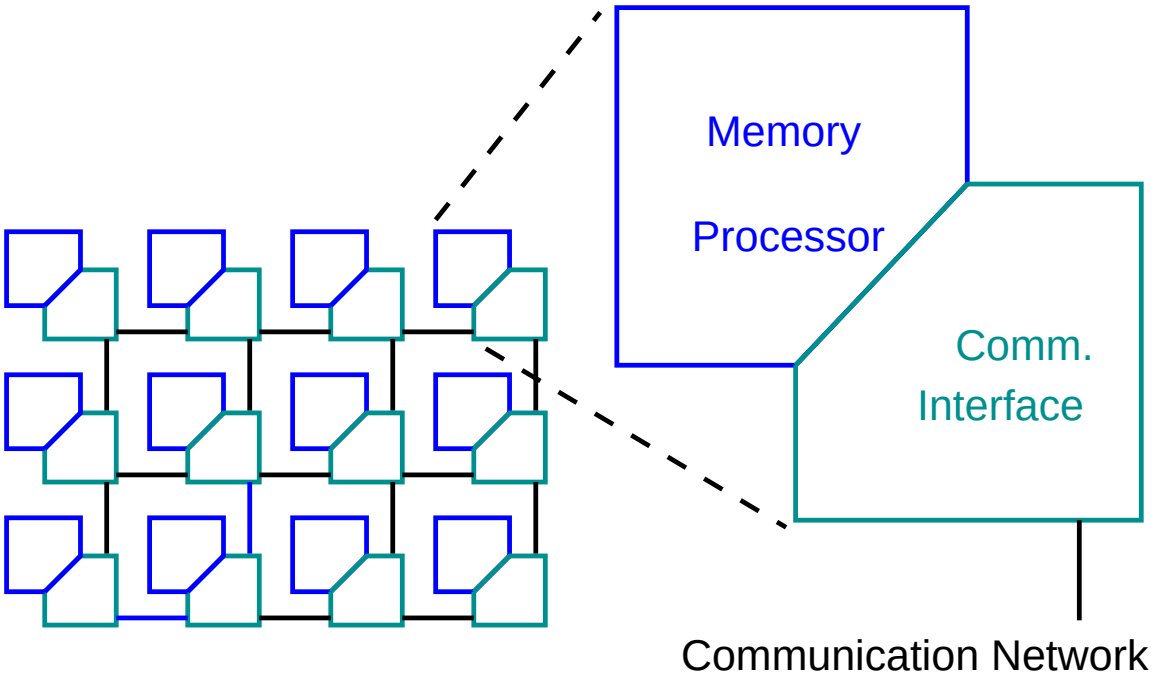
Array of arithmetic units operates and communicates in lock-step.

Shared Memory Multiprocessors



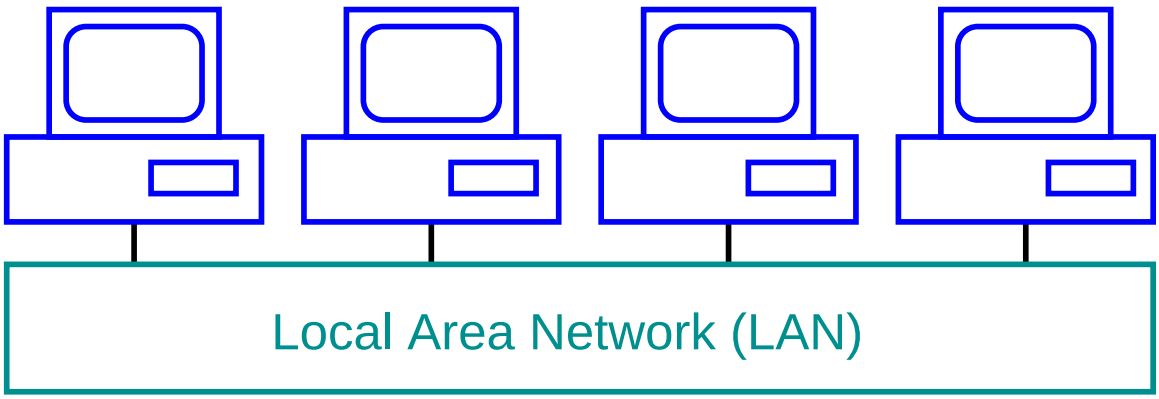
Processors operate asynchronously on shared memory.

Distributed Memory Multiprocessors



Processors operate asynchronously on local memory and communicate by point-to-point network.

Computer Clusters



Cluster of independent computers cooperate via local network.

Literature

- Michael J. Quinn, *Parallel Computing — Theory and Practice*, 2nd edition, McGraw-Hill, New York, 1994.
- Ian T. Foster, *Designing and Building Parallel Programs — Concepts and Tools for Parallel Software Engineering*, Addison Wesley, Reading, Massachusetts, 1995. Online version: `http://www.mcs.anl.gov/dbpp`
- Kai Hwang, *Advanced Computer Architecture: Parallelism, Scalability, Programmability*, McGraw-Hill, New York, 1993.

Course Schedule

Introduction

- Parallel machine models, parallel programming models, a parallel algorithm.

Vector Programming I

- Pipelining, vector architectures, vector instructions, chaining, vector libraries, vector statements, vectorizing compilers.

Vector Programming II

- Convex 3440 architecture and programming, vectorization, strip mining, loop distribution, loop interchange, temporal and spatial parallelism.

Designing Parallel Programs

- Methodological design, partitioning, communication, agglomeration, mapping, modularity, examples.

Course Schedule (Cont'd)

SIMD Programming I

- Data parallelism, SIMD architectures, Maspar MP-1 architecture and programming.

SIMD Programming II

- Mesh algorithms, cube algorithms, mapping strategies.

Performance of Parallel Programs

- Speedup, efficiency, Amdahl's law, scaled speedup, Gustavson's law, load balancing.

Shared Memory Programming I

- Shared memory architectures, automatic scheduling, cache policies, synchronization mechanisms, Unix programming.

Course Schedule (Cont'd)

Shared Memory Programming II

- Sequent Symmetry architecture and programming, micro-tasking library, data decomposition policies, algorithm examples.

Message Passing Programming I

- Distributed memory systems, workstation clusters, static interconnection topologies, switched networks.

Message Passing Programming II

- nCube/2 architecture and programming, whormhole routing, global operations, hypercube algorithms, mapping.

Message Passing Programming III

- Data decomposition methods, row/column oriented algorithms, block-oriented algorithms, global operations.